Release 14.7 - par P.20131013 (nt64)

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Thu Nov 02 10:52:05 2017

INFO: The IO information is provided in three file formats as part of the Place and Route (PAR) process. These formats are:

1. The <design name>\_pad.txt file (this file) designed to provide information on IO usage in a human readable ASCII text format viewable through common text editors.

2. The <design namd>\_pad.csv file for use with spreadsheet programs such as MS Excel. This file can also be read by PACE to communicate post PAR IO information.

3. The <design name>.pad file designed for parsing by customers. It uses the "|" as a data field separator.

INPUT FILE: nbit\_shiftreg\_parallel\_load\_map.ncd

OUTPUT FILE: nbit\_shiftreg\_parallel\_load\_pad.txt

PART TYPE: xc3s500e

SPEED GRADE: -4

PACKAGE: ft256

Pinout by Pin Number:

+---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------+

|Pin Number|Signal Name |Pin Usage|Pin Name |Direction|IO Standard|IO Bank Number|Drive (mA)|Slew Rate|Termination|IOB Delay|Voltage |Constraint|IO Register|Signal Integrity|

+---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------+

|A1 | | |GND | | | | | | | | | | | |

|A2 | | |TDI | | | | | | | | | | | |

|A3 | |IBUF |IP |UNUSED | |0 | | | | | | | | |

|A4 | |DIFFS |IO\_L17N\_0/VREF\_0 |UNUSED | |0 | | | | | | | | |

|A5 | |DIFFM |IO\_L17P\_0 |UNUSED | |0 | | | | | | | | |

|A6 | | |VCCAUX | | | | | | | |2.5 | | | |

|A7 | |IOB |IO |UNUSED | |0 | | | | | | | | |

|A8 | |DIFFMI |IP\_L10P\_0/GCLK8 |UNUSED | |0 | | | | | | | | |

|A9 | |DIFFS |IO\_L09N\_0/GCLK7 |UNUSED | |0 | | | | | | | | |

|A10 |CLK |IBUF |IO\_L09P\_0/GCLK6 |INPUT |LVCMOS25\* |0 | | | |NONE | |LOCATED |NO |NONE |

|A11 | | |VCCAUX | | | | | | | |2.5 | | | |

|A12 |Q\_outputs<3>|IOB |IO |OUTPUT |LVCMOS25\* |0 |12 |SLOW |NONE\*\* | | |LOCATED |NO |NONE |

|A13 |Q\_outputs<4>|IOB |IO\_L03N\_0/VREF\_0 |OUTPUT |LVCMOS25\* |0 |12 |SLOW |NONE\*\* | | |LOCATED |NO |NONE |

|A14 |Q\_outputs<6>|IOB |IO\_L01N\_0 |OUTPUT |LVCMOS25\* |0 |12 |SLOW |NONE\*\* | | |LOCATED |NO |NONE |

|A15 | | |TCK | | | | | | | | | | | |

|A16 | | |GND | | | | | | | | | | | |

|B1 | |DIFFM |IO\_L01P\_3 |UNUSED | |3 | | | | | | | | |

|B2 | |DIFFS |IO\_L01N\_3 |UNUSED | |3 | | | | | | | | |

|B3 | |DIFFS |IO\_L19N\_0/HSWAP |UNUSED | |0 | | | | | | | | |

|B4 | |IOB |IO |UNUSED | |0 | | | | | | | | |

|B5 | | |VCCO\_0 | | |0 | | | | |2.50 | | | |

|B6 | |IBUF |IP |UNUSED | |0 | | | | | | | | |

|B7 | |DIFFM |IO\_L13P\_0 |UNUSED | |0 | | | | | | | | |

|B8 | |DIFFSI |IP\_L10N\_0/GCLK9 |UNUSED | |0 | | | | | | | | |

|B9 | | |GND | | | | | | | | | | | |

|B10 | |IBUF |IP |UNUSED | |0 | | | | | | | | |

|B11 |Q\_outputs<2>|IOB |IO\_L05N\_0/VREF\_0 |OUTPUT |LVCMOS25\* |0 |12 |SLOW |NONE\*\* | | |LOCATED |NO |NONE |

|B12 | | |VCCO\_0 | | |0 | | | | |2.50 | | | |

|B13 |Q\_outputs<5>|IOB |IO\_L03P\_0 |OUTPUT |LVCMOS25\* |0 |12 |SLOW |NONE\*\* | | |LOCATED |NO |NONE |

|B14 |Q\_outputs<7>|IOB |IO\_L01P\_0 |OUTPUT |LVCMOS25\* |0 |12 |SLOW |NONE\*\* | | |LOCATED |NO |NONE |

|B15 | | |TMS | | | | | | | | | | | |

|B16 |D\_inputs<7> |IBUF |IP |INPUT |LVCMOS25\* |1 | | | |NONE | |LOCATED |NO |NONE |

|C1 | |DIFFM |IO\_L02P\_3 |UNUSED | |3 | | | | | | | | |

|C2 | |DIFFS |IO\_L02N\_3/VREF\_3 |UNUSED | |3 | | | | | | | | |

|C3 | |DIFFM |IO\_L19P\_0 |UNUSED | |0 | | | | | | | | |

|C4 | |DIFFS |IO\_L18N\_0 |UNUSED | |0 | | | | | | | | |

|C5 | |DIFFM |IO\_L18P\_0 |UNUSED | |0 | | | | | | | | |

|C6 | |DIFFM |IO\_L15P\_0 |UNUSED | |0 | | | | | | | | |

|C7 | |DIFFS |IO\_L13N\_0 |UNUSED | |0 | | | | | | | | |

|C8 | |DIFFM |IO\_L11P\_0/GCLK10 |UNUSED | |0 | | | | | | | | |

|C9 | |DIFFSI |IP\_L07N\_0 |UNUSED | |0 | | | | | | | | |

|C10 |load\_shift |IBUF |IP\_L07P\_0 |INPUT |LVCMOS25\* |0 | | | |NONE | |LOCATED |NO |NONE |

|C11 |Q\_outputs<0>|IOB |IO\_L05P\_0 |OUTPUT |LVCMOS25\* |0 |12 |SLOW |NONE\*\* | | |LOCATED |YES |NONE |

|C12 |preset |IBUF |IP\_L02N\_0 |INPUT |LVCMOS25\* |0 | | | |NONE | |LOCATED |NO |NONE |

|C13 |shift\_in |IBUF |IP |INPUT |LVCMOS25\* |0 | | | |NONE | |LOCATED |NO |NONE |

|C14 | | |TDO | | | | | | | | | | | |

|C15 | |DIFFS |IO\_L19N\_1/LDC2 |UNUSED | |1 | | | | | | | | |

|C16 | |DIFFM |IO\_L19P\_1/LDC1 |UNUSED | |1 | | | | | | | | |

|D1 | |DIFFM |IO\_L05P\_3 |UNUSED | |3 | | | | | | | | |

|D2 | |IBUF |IP |UNUSED | |3 | | | | | | | | |

|D3 | | |PROG\_B | | | | | | | | | | | |

|D4 | | |VCCINT | | | | | | | |1.2 | | | |

|D5 | |DIFFMI |IP\_L16P\_0 |UNUSED | |0 | | | | | | | | |

|D6 | |DIFFS |IO\_L15N\_0 |UNUSED | |0 | | | | | | | | |

|D7 | |DIFFS |IO\_L14N\_0/VREF\_0 |UNUSED | |0 | | | | | | | | |

|D8 | |DIFFS |IO\_L11N\_0/GCLK11 |UNUSED | |0 | | | | | | | | |

|D9 | |IOB |IO/VREF\_0 |UNUSED | |0 | | | | | | | | |

|D10 | |DIFFM |IO\_L06P\_0 |UNUSED | |0 | | | | | | | | |

|D11 |Q\_outputs<1>|IOB |IO\_L04P\_0 |OUTPUT |LVCMOS25\* |0 |12 |SLOW |NONE\*\* | | |LOCATED |NO |NONE |

|D12 |reset |IBUF |IP\_L02P\_0 |INPUT |LVCMOS25\* |0 | | | |NONE | |LOCATED |NO |NONE |

|D13 | | |VCCINT | | | | | | | |1.2 | | | |

|D14 | |DIFFS |IO\_L18N\_1/LDC0 |UNUSED | |1 | | | | | | | | |

|D15 | |DIFFM |IO\_L18P\_1/HDC |UNUSED | |1 | | | | | | | | |

|D16 |D\_inputs<6> |IBUF |IP/VREF\_1 |INPUT |LVCMOS25\* |1 | | | |NONE | |LOCATED |NO |NONE |

|E1 | |DIFFS |IO\_L05N\_3 |UNUSED | |3 | | | | | | | | |

|E2 | | |VCCO\_3 | | |3 | | | | |any\*\*\*\*\*\*| | | |

|E3 | |DIFFM |IO\_L03P\_3 |UNUSED | |3 | | | | | | | | |

|E4 | |DIFFS |IO\_L03N\_3 |UNUSED | |3 | | | | | | | | |

|E5 | | |VCCINT | | | | | | | |1.2 | | | |

|E6 | |DIFFSI |IP\_L16N\_0 |UNUSED | |0 | | | | | | | | |

|E7 | |DIFFM |IO\_L14P\_0 |UNUSED | |0 | | | | | | | | |

|E8 | |DIFFM |IO\_L12P\_0 |UNUSED | |0 | | | | | | | | |

|E9 | |DIFFM |IO\_L08P\_0/GCLK4 |UNUSED | |0 | | | | | | | | |

|E10 | |DIFFS |IO\_L06N\_0 |UNUSED | |0 | | | | | | | | |

|E11 | |DIFFS |IO\_L04N\_0 |UNUSED | |0 | | | | | | | | |

|E12 | | |VCCINT | | | | | | | |1.2 | | | |

|E13 | |DIFFM |IO\_L17P\_1 |UNUSED | |1 | | | | | | | | |

|E14 |D\_inputs<5> |IBUF |IP |INPUT |LVCMOS25\* |1 | | | |NONE | |LOCATED |NO |NONE |

|E15 | | |VCCO\_1 | | |1 | | | | |2.50 | | | |

|E16 | |DIFFS |IO\_L17N\_1 |UNUSED | |1 | | | | | | | | |

|F1 | | |VCCAUX | | | | | | | |2.5 | | | |

|F2 | |IBUF |IP |UNUSED | |3 | | | | | | | | |

|F3 | |DIFFM |IO\_L04P\_3 |UNUSED | |3 | | | | | | | | |

|F4 | |DIFFS |IO\_L04N\_3/VREF\_3 |UNUSED | |3 | | | | | | | | |

|F5 | |IOB |IO |UNUSED | |3 | | | | | | | | |

|F6 | | |GND | | | | | | | | | | | |

|F7 | | |VCCO\_0 | | |0 | | | | |2.50 | | | |

|F8 | |DIFFS |IO\_L12N\_0 |UNUSED | |0 | | | | | | | | |

|F9 | |DIFFS |IO\_L08N\_0/GCLK5 |UNUSED | |0 | | | | | | | | |

|F10 | | |VCCO\_0 | | |0 | | | | |2.50 | | | |

|F11 | | |GND | | | | | | | | | | | |

|F12 | |DIFFS |IO\_L16N\_1 |UNUSED | |1 | | | | | | | | |

|F13 | |DIFFM |IO\_L16P\_1 |UNUSED | |1 | | | | | | | | |

|F14 | |DIFFM |IO\_L15P\_1 |UNUSED | |1 | | | | | | | | |

|F15 | |DIFFS |IO\_L15N\_1 |UNUSED | |1 | | | | | | | | |

|F16 | | |VCCAUX | | | | | | | |2.5 | | | |

|G1 | |IBUF |IP/VREF\_3 |UNUSED | |3 | | | | | | | | |

|G2 | |DIFFS |IO\_L07N\_3 |UNUSED | |3 | | | | | | | | |

|G3 | |DIFFM |IO\_L07P\_3 |UNUSED | |3 | | | | | | | | |

|G4 | |DIFFS |IO\_L06N\_3 |UNUSED | |3 | | | | | | | | |

|G5 | |DIFFM |IO\_L06P\_3 |UNUSED | |3 | | | | | | | | |

|G6 | | |VCCO\_3 | | |3 | | | | |any\*\*\*\*\*\*| | | |

|G7 | | |GND | | | | | | | | | | | |

|G8 | | |GND | | | | | | | | | | | |

|G9 | | |GND | | | | | | | | | | | |

|G10 | | |GND | | | | | | | | | | | |

|G11 | | |VCCO\_1 | | |1 | | | | |2.50 | | | |

|G12 |D\_inputs<4> |IBUF |IP |INPUT |LVCMOS25\* |1 | | | |NONE | |LOCATED |NO |NONE |

|G13 | |DIFFM |IO\_L14P\_1 |UNUSED | |1 | | | | | | | | |

|G14 | |DIFFS |IO\_L14N\_1/A0 |UNUSED | |1 | | | | | | | | |

|G15 | |DIFFM |IO\_L13P\_1/A2 |UNUSED | |1 | | | | | | | | |

|G16 | |DIFFS |IO\_L13N\_1/A1 |UNUSED | |1 | | | | | | | | |

|H1 | |IBUF |IP |UNUSED | |3 | | | | | | | | |

|H2 | | |GND | | | | | | | | | | | |

|H3 | |DIFFM |IO\_L09P\_3/LHCLK2 |UNUSED | |3 | | | | | | | | |

|H4 | |DIFFS |IO\_L09N\_3/LHCLK3/IRDY2 |UNUSED | |3 | | | | | | | | |

|H5 | |DIFFM |IO\_L08P\_3/LHCLK0 |UNUSED | |3 | | | | | | | | |

|H6 | |DIFFS |IO\_L08N\_3/LHCLK1 |UNUSED | |3 | | | | | | | | |

|H7 | | |GND | | | | | | | | | | | |

|H8 | | |GND | | | | | | | | | | | |

|H9 | | |GND | | | | | | | | | | | |

|H10 | | |GND | | | | | | | | | | | |

|H11 | |DIFFS |IO\_L12N\_1/A3/RHCLK7 |UNUSED | |1 | | | | | | | | |

|H12 | |DIFFM |IO\_L12P\_1/A4/RHCLK6 |UNUSED | |1 | | | | | | | | |

|H13 |D\_inputs<3> |IBUF |IP/VREF\_1 |INPUT |LVCMOS25\* |1 | | | |NONE | |LOCATED |NO |NONE |

|H14 | |DIFFS |IO\_L11N\_1/A5/RHCLK5 |UNUSED | |1 | | | | | | | | |

|H15 | |DIFFM |IO\_L11P\_1/A6/RHCLK4/IRDY1|UNUSED | |1 | | | | | | | | |

|H16 |D\_inputs<2> |IBUF |IP |INPUT |LVCMOS25\* |1 | | | |NONE | |LOCATED |NO |NONE |

|J1 | |DIFFM |IO\_L12P\_3 |UNUSED | |3 | | | | | | | | |

|J2 | |DIFFM |IO\_L10P\_3/LHCLK4/TRDY2 |UNUSED | |3 | | | | | | | | |

|J3 | |DIFFS |IO\_L10N\_3/LHCLK5 |UNUSED | |3 | | | | | | | | |

|J4 | |DIFFS |IO\_L11N\_3/LHCLK7 |UNUSED | |3 | | | | | | | | |

|J5 | |DIFFM |IO\_L11P\_3/LHCLK6 |UNUSED | |3 | | | | | | | | |

|J6 | |IBUF |IP |UNUSED | |3 | | | | | | | | |

|J7 | | |GND | | | | | | | | | | | |

|J8 | | |GND | | | | | | | | | | | |

|J9 | | |GND | | | | | | | | | | | |

|J10 | | |GND | | | | | | | | | | | |

|J11 |D\_inputs<0> |IBUF |IP |INPUT |LVCMOS25\* |1 | | | |NONE | |LOCATED |NO |NONE |

|J12 |D\_inputs<1> |IBUF |IP |INPUT |LVCMOS25\* |1 | | | |NONE | |LOCATED |NO |NONE |

|J13 | |DIFFS |IO\_L10N\_1/A7/RHCLK3/TRDY1|UNUSED | |1 | | | | | | | | |

|J14 | |DIFFM |IO\_L10P\_1/A8/RHCLK2 |UNUSED | |1 | | | | | | | | |

|J15 | | |GND | | | | | | | | | | | |

|J16 | |DIFFS |IO\_L09N\_1/A9/RHCLK1 |UNUSED | |1 | | | | | | | | |

|K1 | |DIFFS |IO\_L12N\_3 |UNUSED | |3 | | | | | | | | |

|K2 | |DIFFM |IO\_L13P\_3 |UNUSED | |3 | | | | | | | | |

|K3 | |DIFFS |IO\_L13N\_3 |UNUSED | |3 | | | | | | | | |

|K4 | |IBUF |IP |UNUSED | |3 | | | | | | | | |

|K5 | |DIFFM |IO\_L15P\_3 |UNUSED | |3 | | | | | | | | |

|K6 | | |VCCO\_3 | | |3 | | | | |any\*\*\*\*\*\*| | | |

|K7 | | |GND | | | | | | | | | | | |

|K8 | | |GND | | | | | | | | | | | |

|K9 | | |GND | | | | | | | | | | | |

|K10 | | |GND | | | | | | | | | | | |

|K11 | | |VCCO\_1 | | |1 | | | | |2.50 | | | |

|K12 | |DIFFS |IO\_L07N\_1/A11 |UNUSED | |1 | | | | | | | | |

|K13 | |DIFFM |IO\_L07P\_1/A12 |UNUSED | |1 | | | | | | | | |

|K14 | |DIFFS |IO\_L08N\_1/VREF\_1 |UNUSED | |1 | | | | | | | | |

|K15 | |DIFFM |IO\_L08P\_1 |UNUSED | |1 | | | | | | | | |

|K16 | |DIFFM |IO\_L09P\_1/A10/RHCLK0 |UNUSED | |1 | | | | | | | | |

|L1 | | |VCCAUX | | | | | | | |2.5 | | | |

|L2 | |DIFFS |IO\_L14N\_3/VREF\_3 |UNUSED | |3 | | | | | | | | |

|L3 | |DIFFM |IO\_L14P\_3 |UNUSED | |3 | | | | | | | | |

|L4 | |DIFFS |IO\_L17N\_3 |UNUSED | |3 | | | | | | | | |

|L5 | |DIFFS |IO\_L15N\_3 |UNUSED | |3 | | | | | | | | |

|L6 | | |GND | | | | | | | | | | | |

|L7 | | |VCCO\_2 | | |2 | | | | |any\*\*\*\*\*\*| | | |

|L8 | |DIFFS |IO\_L09N\_2/D6/GCLK13 |UNUSED | |2 | | | | | | | | |

|L9 | |DIFFM |IO\_L13P\_2/M0 |UNUSED | |2 | | | | | | | | |

|L10 | | |VCCO\_2 | | |2 | | | | |any\*\*\*\*\*\*| | | |

|L11 | | |GND | | | | | | | | | | | |

|L12 | |DIFFM |IO\_L05P\_1 |UNUSED | |1 | | | | | | | | |

|L13 | |DIFFS |IO\_L05N\_1 |UNUSED | |1 | | | | | | | | |

|L14 | |DIFFM |IO\_L06P\_1 |UNUSED | |1 | | | | | | | | |

|L15 | |DIFFS |IO\_L06N\_1 |UNUSED | |1 | | | | | | | | |

|L16 | | |VCCAUX | | | | | | | |2.5 | | | |

|M1 | |DIFFM |IO\_L16P\_3 |UNUSED | |3 | | | | | | | | |

|M2 | | |VCCO\_3 | | |3 | | | | |any\*\*\*\*\*\*| | | |

|M3 | |IBUF |IP |UNUSED | |3 | | | | | | | | |

|M4 | |DIFFM |IO\_L17P\_3 |UNUSED | |3 | | | | | | | | |

|M5 | | |VCCINT | | | | | | | |1.2 | | | |

|M6 | |DIFFM |IO\_L05P\_2 |UNUSED | |2 | | | | | | | | |

|M7 | |IBUF |IP |UNUSED | |2 | | | | | | | | |

|M8 | |DIFFM |IO\_L09P\_2/D7/GCLK12 |UNUSED | |2 | | | | | | | | |

|M9 | |DIFFS |IO\_L13N\_2/DIN/D0 |UNUSED | |2 | | | | | | | | |

|M10 | |DIFFS |IO\_L15N\_2 |UNUSED | |2 | | | | | | | | |

|M11 | |DIFFSI |IP\_L17N\_2 |UNUSED | |2 | | | | | | | | |

|M12 | | |VCCINT | | | | | | | |1.2 | | | |

|M13 | |IBUF |IP |UNUSED | |1 | | | | | | | | |

|M14 | |IOB |IO |UNUSED | |1 | | | | | | | | |

|M15 | | |VCCO\_1 | | |1 | | | | |2.50 | | | |

|M16 | |DIFFS |IO\_L04N\_1/VREF\_1 |UNUSED | |1 | | | | | | | | |

|N1 | |DIFFS |IO\_L16N\_3 |UNUSED | |3 | | | | | | | | |

|N2 | |IOB |IO/VREF\_3 |UNUSED | |3 | | | | | | | | |

|N3 | |IBUF |IP |UNUSED | |3 | | | | | | | | |

|N4 | | |VCCINT | | | | | | | |1.2 | | | |

|N5 | |DIFFS |IO\_L03N\_2/MOSI/CSI\_B |UNUSED | |2 | | | | | | | | |

|N6 | |DIFFS |IO\_L05N\_2 |UNUSED | |2 | | | | | | | | |

|N7 | |DIFFM |IO\_L07P\_2 |UNUSED | |2 | | | | | | | | |

|N8 | |DIFFM |IO\_L10P\_2/D4/GCLK14 |UNUSED | |2 | | | | | | | | |

|N9 | |DIFFS |IO\_L12N\_2/D1/GCLK3 |UNUSED | |2 | | | | | | | | |

|N10 | |DIFFM |IO\_L15P\_2 |UNUSED | |2 | | | | | | | | |

|N11 | |DIFFMI |IP\_L17P\_2 |UNUSED | |2 | | | | | | | | |

|N12 | |DIFFS |IO\_L18N\_2/A20 |UNUSED | |2 | | | | | | | | |

|N13 | | |VCCINT | | | | | | | |1.2 | | | |

|N14 | |DIFFM |IO\_L03P\_1 |UNUSED | |1 | | | | | | | | |

|N15 | |DIFFS |IO\_L03N\_1/VREF\_1 |UNUSED | |1 | | | | | | | | |

|N16 | |DIFFM |IO\_L04P\_1 |UNUSED | |1 | | | | | | | | |

|P1 | |DIFFS |IO\_L18N\_3 |UNUSED | |3 | | | | | | | | |

|P2 | |DIFFM |IO\_L18P\_3 |UNUSED | |3 | | | | | | | | |

|P3 | |DIFFM |IO\_L01P\_2/CSO\_B |UNUSED | |2 | | | | | | | | |

|P4 | |DIFFS |IO\_L01N\_2/INIT\_B |UNUSED | |2 | | | | | | | | |

|P5 | |DIFFM |IO\_L03P\_2/DOUT/BUSY |UNUSED | |2 | | | | | | | | |

|P6 | |DIFFS |IO\_L06N\_2 |UNUSED | |2 | | | | | | | | |

|P7 | |DIFFS |IO\_L07N\_2 |UNUSED | |2 | | | | | | | | |

|P8 | |DIFFS |IO\_L10N\_2/D3/GCLK15 |UNUSED | |2 | | | | | | | | |

|P9 | |DIFFM |IO\_L12P\_2/D2/GCLK2 |UNUSED | |2 | | | | | | | | |

|P10 | |DIFFM |IO\_L14P\_2 |UNUSED | |2 | | | | | | | | |

|P11 | |DIFFS |IO\_L16N\_2/A22 |UNUSED | |2 | | | | | | | | |

|P12 | |DIFFM |IO\_L18P\_2/A21 |UNUSED | |2 | | | | | | | | |

|P13 | |IOB |IO/VREF\_2 |UNUSED | |2 | | | | | | | | |

|P14 | |DIFFM |IO\_L20P\_2/VS0/A17 |UNUSED | |2 | | | | | | | | |

|P15 | |DIFFS |IO\_L02N\_1/A13 |UNUSED | |1 | | | | | | | | |

|P16 | |DIFFM |IO\_L02P\_1/A14 |UNUSED | |1 | | | | | | | | |

|R1 | |DIFFS |IO\_L19N\_3 |UNUSED | |3 | | | | | | | | |

|R2 | |DIFFM |IO\_L19P\_3 |UNUSED | |3 | | | | | | | | |

|R3 | |DIFFSI |IP\_L02N\_2 |UNUSED | |2 | | | | | | | | |

|R4 | |IOB |IO/VREF\_2 |UNUSED | |2 | | | | | | | | |

|R5 | | |VCCO\_2 | | |2 | | | | |any\*\*\*\*\*\*| | | |

|R6 | |DIFFM |IO\_L06P\_2 |UNUSED | |2 | | | | | | | | |

|R7 | |DIFFMI |IP\_L08P\_2 |UNUSED | |2 | | | | | | | | |

|R8 | | |GND | | | | | | | | | | | |

|R9 | |DIFFSI |IP\_L11N\_2/M2/GCLK1 |UNUSED | |2 | | | | | | | | |

|R10 | |DIFFS |IO\_L14N\_2/VREF\_2 |UNUSED | |2 | | | | | | | | |

|R11 | |DIFFM |IO\_L16P\_2/A23 |UNUSED | |2 | | | | | | | | |

|R12 | | |VCCO\_2 | | |2 | | | | |any\*\*\*\*\*\*| | | |

|R13 | |DIFFS |IO\_L19N\_2/VS1/A18 |UNUSED | |2 | | | | | | | | |

|R14 | |DIFFS |IO\_L20N\_2/CCLK |UNUSED | |2 | | | | | | | | |

|R15 | |DIFFS |IO\_L01N\_1/A15 |UNUSED | |1 | | | | | | | | |

|R16 | |DIFFM |IO\_L01P\_1/A16 |UNUSED | |1 | | | | | | | | |

|T1 | | |GND | | | | | | | | | | | |

|T2 | |IBUF |IP |UNUSED | |2 | | | | | | | | |

|T3 | |DIFFMI |IP\_L02P\_2 |UNUSED | |2 | | | | | | | | |

|T4 | |DIFFM |IO\_L04P\_2 |UNUSED | |2 | | | | | | | | |

|T5 | |DIFFS |IO\_L04N\_2 |UNUSED | |2 | | | | | | | | |

|T6 | | |VCCAUX | | | | | | | |2.5 | | | |

|T7 | |DIFFSI |IP\_L08N\_2/VREF\_2 |UNUSED | |2 | | | | | | | | |

|T8 | |IOB |IO/D5 |UNUSED | |2 | | | | | | | | |

|T9 | |DIFFMI |IP\_L11P\_2/RDWR\_B/GCLK0 |UNUSED | |2 | | | | | | | | |

|T10 | |IOB |IO/M1 |UNUSED | |2 | | | | | | | | |

|T11 | | |VCCAUX | | | | | | | |2.5 | | | |

|T12 | |IBUF |IP |UNUSED | |2 | | | | | | | | |

|T13 | |DIFFM |IO\_L19P\_2/VS2/A19 |UNUSED | |2 | | | | | | | | |

|T14 | |IBUF |IP |UNUSED | |2 | | | | | | | | |

|T15 | | |DONE | | | | | | | | | | | |

|T16 | | |GND | | | | | | | | | | | |

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\* Default value.

\*\* This default Pullup/Pulldown value can be overridden in Bitgen.

\*\*\*\*\*\* Special VCCO requirements may apply. Please consult the device

family datasheet for specific guideline on VCCO requirements.